

PODC 2000 TLA⁺ Tutorial

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┌────────────────────────── MODULE Mutex ───────────────────────────┐
CONSTANT Proc
VARIABLE state
└──────────────────────────┬──────────────────────────┘
Init ≜ state = [p ∈ Proc ↦ “idle”]
Request(p) ≜ ∧ state[p] = “idle”
                ∧ state' = [state EXCEPT ![p] = “waiting”]
Acquire(p) ≜ ∧ state[p] = “waiting”
                ∧ ∀ q ∈ Proc : state[q] ≠ “owner”
                ∧ state' = [state EXCEPT ![p] = “owner”]
Release(p) ≜ ∧ state[p] = “owner”
                ∧ state' = [state EXCEPT ![p] = “idle”]
Next ≜ ∃ p ∈ Proc : Request(p) ∨ Acquire(p) ∨ Release(p)
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StarvationFree ≜ ∀ p ∈ Proc : SFstate(Acquire(p))
Spec ≜ Init ∧ □[Next]state ∧ StarvationFree
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----- MODULE Mutex -----
CONSTANT Proc
VARIABLE state
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Init == state = [p \in Proc |-> "idle"]

Request(p) == /\ state[p] = "idle"
              /\ state' = [state EXCEPT ![p] = "waiting"]

Acquire(p) == /\ state[p] = "waiting"
              /\ \A q \in Proc : state[q] # "owner"
              /\ state' = [state EXCEPT ![p] = "owner"]

Release(p) == /\ state[p] = "owner"
              /\ state' = [state EXCEPT ![p] = "idle"]

Next == \E p \in Proc : Request(p) \/ Acquire(p) \/ Release(p)
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StarvationFree == \A p \in Proc : SF_state(Acquire(p))

Spec == Init /\ [] [Next]_state /\ StarvationFree
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MODULE *TimeClocks*

EXTENDS *Naturals, Sequences*

CONSTANT *Proc*, \ll

ASSUME $\forall p \in Proc : \wedge \neg p \ll p$
 $\wedge \forall q \in Proc \setminus \{p\} : (p \ll q) \vee (q \ll p)$
 $\wedge \forall q, r \in Proc : (p \ll q) \wedge (q \ll r) \Rightarrow (p \ll r)$

$a \prec b \triangleq \vee a.TS < b.TS$
 $\vee (a.TS = b.TS) \wedge (a.proc \ll b.proc)$

VARIABLES *state, msgQ, reqSet, clock, lastTSent, lastTRcvd*

vars $\triangleq \langle state, msgQ, reqSet, clock, lastTSent, lastTRcvd \rangle$

Init $\triangleq \wedge state = [p \in Proc \mapsto \text{“idle”}]$
 $\wedge msgQ = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto \langle \rangle]]$
 $\wedge reqSet = [p \in Proc \mapsto \{\}]$
 $\wedge clock \in [Proc \rightarrow Nat]$
 $\wedge lastTSent = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto 0]]$
 $\wedge lastTRcvd = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto 0]]$

Request(p) \triangleq
 $\wedge state[p] = \text{“idle”}$
 $\wedge state' = [state \text{ EXCEPT } ![p] = \text{“waiting”}]$
 $\wedge \exists n \in Nat :$
 $\wedge clock' = [clock \text{ EXCEPT } ![p] = n]$
 $\wedge n > clock[p]$
 $\wedge \text{LET } msg \triangleq [TS \mapsto n, proc \mapsto p, cmd \mapsto \text{“acquire”}]$
 $\text{IN } \wedge msgQ' = [msgQ \text{ EXCEPT } ![p] =$
 $\quad [q \in Proc \setminus \{p\} \mapsto Append(@[q], msg)]]$
 $\wedge reqSet' = [reqSet \text{ EXCEPT } ![p] = @ \cup \{msg\}]$
 $\wedge lastTSent' = [lastTSent \text{ EXCEPT } ![p] = [q \in Proc \setminus \{p\} \mapsto n]]$
 $\wedge \text{UNCHANGED } lastTRcvd$

Acquire(p) \triangleq
 $\text{LET } pReq \triangleq \text{CHOOSE } req \in reqSet[p] : req.proc = p$
 $\text{IN } \wedge state[p] = \text{“waiting”}$
 $\wedge \forall req \in reqSet[p] \setminus \{pReq\} : pReq \prec req$
 $\wedge \forall q \in Proc \setminus \{p\} : pReq \prec [TS \mapsto lastTRcvd[p][q] + 1, proc \mapsto q]$
 $\wedge state' = [state \text{ EXCEPT } ![p] = \text{“owner”}]$
 $\wedge reqSet' = [reqSet \text{ EXCEPT } ![p] = @ \setminus \{pReq\}]$
 $\wedge \text{UNCHANGED } \langle msgQ, clock, lastTSent, lastTRcvd \rangle$

MODULE <i>InnerSM</i>
EXTENDS <i>SMPParams</i> , <i>Sequences</i>
VARIABLES <i>state</i> , <i>pending</i> , <i>rcvd</i>
$\text{vars} \triangleq \langle \text{int}, \text{state}, \text{pending}, \text{rcvd} \rangle$
$\begin{aligned} \text{Init} \triangleq & \wedge \text{state} = [p \in \text{Proc} \mapsto \text{InitState}] \\ & \wedge \text{pending} = [p \in \text{Proc} \mapsto \langle \rangle] \\ & \wedge \text{rcvd} = [p \in \text{Proc} \mapsto \langle \rangle] \end{aligned}$
$\begin{aligned} \text{IssueCmd}(p, c) \triangleq & \\ & \wedge \text{Request}(\text{int}, \text{int}', c, p) \\ & \wedge \text{rcvd}' = [\text{rcvd} \text{ EXCEPT } ![p] = \text{Append}(\text{@}, c)] \\ & \wedge \text{UNCHANGED} \langle \text{state}, \text{pending} \rangle \end{aligned}$
$\begin{aligned} \text{SequenceCmd}(p) \triangleq & \\ & \wedge \text{rcvd}[p] \neq \langle \rangle \\ & \wedge \text{rcvd}' = [\text{rcvd} \text{ EXCEPT } ![p] = \text{Tail}(\text{@})] \\ & \wedge \text{pending}' = [q \in \text{Proc} \mapsto \text{Append}(\text{pending}[q], \text{Head}(\text{rcvd}[p]))] \\ & \wedge \text{UNCHANGED} \langle \text{int}, \text{state} \rangle \end{aligned}$
$\begin{aligned} \text{IssueReply}(p) \triangleq & \\ & \wedge \text{pending}[p] \neq \langle \rangle \\ & \wedge \text{Reply}(\text{int}, \text{int}', \text{Output}(\text{state}[p], \text{Head}(\text{pending}[p])), p) \\ & \wedge \text{state}' = [\text{state} \text{ EXCEPT } ![p] = \text{NewState}(\text{@}, \text{Head}(\text{pending}[p]))] \\ & \wedge \text{pending}' = [\text{pending} \text{ EXCEPT } ![p] = \text{Tail}(\text{@})] \\ & \wedge \text{UNCHANGED} \text{rcvd} \end{aligned}$
$\begin{aligned} \text{Next} \triangleq & \exists p \in \text{Proc} : \vee \exists c \in \text{Cmd} : \text{IssueCmd}(p, c) \\ & \vee \text{SequenceCmd}(p) \\ & \vee \text{IssueReply}(p) \end{aligned}$
$\begin{aligned} \text{ISpec} \triangleq & \wedge \text{Init} \\ & \wedge \square [\text{Next}]_{\text{vars}} \\ & \wedge \forall p \in \text{Proc} : \wedge \text{WF}_{\text{vars}}(\text{SequenceCmd}(p)) \\ & \wedge \text{WF}_{\text{vars}}(\text{IssueReply}(p)) \end{aligned}$

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┌────────────────────────── MODULE MutexSM ───────────────────────────┐
VARIABLE int
CONSTANTS Proc, Request(-, -, -, -), Reply(-, -, -, -)
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InitState  $\triangleq$  [free  $\mapsto$  TRUE, waiting  $\mapsto$   $\langle \rangle$ ]

NewState(s, c)  $\triangleq$ 
  IF c.cmd = “acquire”
  THEN IF s.free THEN [s EXCEPT !.free = FALSE]
           ELSE [s EXCEPT !.waiting = Append(@, c.proc)]
  ELSE IF s.waiting =  $\langle \rangle$  THEN [s EXCEPT !.free = TRUE]
           ELSE [s EXCEPT !.waiting = Tail(@)]

None  $\triangleq$  CHOOSE v : v  $\notin$  Proc

Output(s, c)  $\triangleq$ 
  IF c.cmd = “acquire” THEN IF s.free THEN c.proc
                               ELSE None
  ELSE IF s.waiting  $\neq$   $\langle \rangle$  THEN Head(s.waiting)
  ELSE None

Cmd  $\triangleq$  [cmd : {“acquire”, “release”}, proc : Proc]
INSTANCE StateMachine
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┌────────────────────────── MODULE MutexUser ───────────────────────────┐
VARIABLE int
CONSTANTS Request(-, -, -, -), Reply(-, -, -, -), Proc
┌────────────────────────── MODULE Inner ───────────────────────────┐
VARIABLE state
None  $\triangleq$  CHOOSE  $v : v \notin Proc$ 
Init  $\triangleq state = [p \in Proc \mapsto \text{"idle"}]$ 
IssueAcq( $p$ )  $\triangleq \wedge state[p] = \text{"idle"}$ 
                $\wedge Request(int, int', [cmd \mapsto \text{"acquire"}, proc \mapsto p], p)$ 
                $\wedge state' = [state \text{ EXCEPT } ![p] = \text{"waiting"}]$ 
IssueRel( $p$ )  $\triangleq \wedge state[p] = \text{"owner"}$ 
                $\wedge Request(int, int' [cmd \mapsto \text{"release"}, proc \mapsto p], p)$ 
                $\wedge state' = [state \text{ EXCEPT } ![p] = \text{"idle"}]$ 
RcvReply( $p$ )  $\triangleq \exists r \in Proc \cup \{None\} :$ 
                $\wedge Reply(int, int', r, p)$ 
                $\wedge state' = \text{IF } r = p$ 
                   THEN  $[state \text{ EXCEPT } ![p] = \text{"owner"}]$ 
                   ELSE  $state$ 
Next  $\triangleq \exists p \in Proc : IssueAcq(p) \vee IssueRel(p) \vee RcvReply(p)$ 
ISpec  $\triangleq Init \wedge \square [Next]_{\langle int, state \rangle}$ 
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I( $state$ )  $\triangleq$  INSTANCE Inner
Spec  $\triangleq \exists state : I(state)!ISpec$ 
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┌────────────────── MODULE MutexSystem ───────────────────┐
VARIABLE int
CONSTANTS Request(-, -, -, -), Reply(-, -, -, -), Proc
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Env  $\triangleq$  INSTANCE MutexUser
Sys  $\triangleq$  INSTANCE MutexSM

ClosedSystem  $\triangleq$  Env!Spec  $\wedge$  Sys!Spec
OpenSystem  $\triangleq$  Env!Spec  $\pm\triangleright$  Sys!Spec
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TLA home page

<http://www.research.digital.com/SRC/tla/>

Handout

<http://www.research.digital.com/SRC/tla/podc00-handout.ps>